

SINGLE EA INSTRUCTION FORMAT

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
X	X	X	X	X	X	X	X	X	X	EFFECTIVE ADDRESS					
											MODE	REGISTER			

BRIEF FORMAT EXTENSION WORD

15	14	12	11	10	9	8	7	0						
D/A	REGISTER		W/L	SCALE		0	DISPLACEMENT							

FULL FORMAT EXTENSION WORD(S)

15	14	12	11	10	9	8	7	6	5	4	3	2	0	
D/A	REGISTER		W/L	SCALE	1	BS	IS	BD SIZE	0	I/IS				
BASE DISPLACEMENT (0, 1, OR 2 WORDS)														

Field	Definition	Field	Definition
Instruction Register Extension Register D/A	General Register Number	BS	Base Register Suppress 0 = Base Register Added 1 = Base Register Suppressed
Register D/A	Index Register Number	IS	Index Suppress 0 = Evaluate and Add Index Operand 1 = Suppress Index Operand
W/L	Index Register Type 0 = Dn 1 = An	BD SIZE	Base Displacement Size 00 = Reserved 01 = Null Displacement 10 = Word Displacement 11 = Long-Word Displacement
Scale	Word/Long Word Index Size 0 = Sign-Extended Word 1 = Long Word	I/IS *	Index/Indirect Selection Indirect and Indexing Operand Determined in Conjunction with Bit 6, Index Suppress
	Scale Factor 00 = 1 01 = 2 10 = 4 11 = 8		

\*Memory indirect addressing will cause illegal instruction trap; must be = 000 if IS = 1