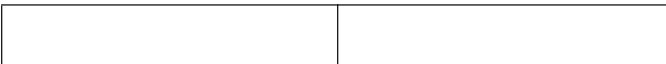


31

16 15

0



A7# (SSP)

SUPERVISOR STACK
POINTER

15

8 7

0



SR

STATUS REGISTER

31

0



PC

VECTOR BASE REGISTER

31

3 2

0



SFC

ALTERNATE FUNCTION

DFC

CODE REGISTERS