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		PCR	PROCESSOR CONFIGURATION REGISTER
		A7/SSP	SUPERVISOR STACK POINTER
		SR	STATUS REGISTER (CCR IS ALSO SHOWN IN THE USER PROGRAMMING MODEL)
		VBR	VECTOR BASE REGISTER
		SFC	SOURCE FUNCTION CODE
		DFC	DESTINATION FUNCTION CODE
		CACR	CACHE CONTROL REGISTER
		URP	USER ROOT POINTER REGISTER
		SRP	SUPERVISOR ROOT POINTER REGISTER
		TC	TRANSLATION CONTROL REGISTER
		DTT0	DATA TRANSPARENT TRANSLATION REGISTER 0
		DTT1	DATA TRANSPARENT TRANSLATION REGISTER 1
		ITT0	INSTRUCTION TRANSPARENT TRANSLATION REGISTER 0
		ITT1	INSTRUCTION TRANSPARENT TRANSLATION REGISTER 1
		BUSCR	BUS CONTROL REGISTER